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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/706,068	11/03/2000	Abdellatif Bellaouar	TI-31011	6639
75	90 11/30/2004		EXAMINER	
Ronald O Neerings			CHANG, EDITH M	
Texas Instrument			ART UNIT PAPER NUMBER	
Dallas, TX 75265			2637	

DATE MAILED: 11/30/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

			AL			
	Application No.	Applicant(s)				
	09/706,068	BELLAOUAR, ABDELLA	ATIF			
Office Action Summary	Examiner	Art Unit				
	Edith M Chang	2637				
The MAILING DATE of this communica Period for Reply	tion appears on the cover sheet w	th the correspondence address	;			
A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICA - Extensions of time may be available under the provisions of 3 after SIX (6) MONTHS from the mailing date of this communication of the period for reply specified above is less than thirty (30) of the No period for reply is specified above, the maximum statutes Failure to reply within the set or extended period for reply will Any reply received by the Office later than three months after earned patent term adjustment. See 37 CFR 1.704(b).	ATION. 7 CFR 1.136(a). In no event, however, may a cation. 8 ays, a reply within the statutory minimum of thir ory period will apply and will expire SIX (6) MON, by statute, cause the application to become Al	reply be timely filed by (30) days will be considered timely. ITHS from the mailing date of this communi BANDONED (35 U.S.C. § 133).	ication.			
Status						
1) Responsive to communication(s) filed	on 16 November 2004.					
,— .	☐ This action is non-final.					
3) Since this application is in condition for		ers, prosecution as to the mer	its is			
closed in accordance with the practice	· · · · · · · · · · · · · · · · · · ·	·				
Disposition of Claims						
4) ☐ Claim(s) 1-20 is/are pending in the approach 4a) Of the above claim(s) is/are 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-20 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction	withdrawn from consideration.					
Application Papers						
9)☐ The specification is objected to by the E	Examiner.					
10) ☐ The drawing(s) filed on is/are: a	D)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection	on to the drawing(s) be held in abeya	nce. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including th						
11)☐ The oath or declaration is objected to b	y the Examiner. Note the attache	d Office Action or form PTO-15	52.			
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for a) All b) Some * c) None of: 1. Certified copies of the priority do 2. Certified copies of the priority do 3. Copies of the certified copies of application from the Internationa * See the attached detailed Office action for	ocuments have been received. Ocuments have been received in A Ocuments have beer	Application No received in this National Stage	e			
Attachment(s)	_	•				
1) Notice of References Cited (PTO-892)		Summary (PTO-413) s)/Mail Date				
 Notice of Draftsperson's Patent Drawing Review (PTC 3) Information Disclosure Statement(s) (PTO-1449 or PT Paper No(s)/Mail Date 		nformal Patent Application (PTO-152) 				

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DETAILED ACTION

Response to Arguments

1. Applicant's arguments, see pages 2-4, filed November 16, 2004, with respect to the rejection(s) of claim(s) 1-20 under 35 U.S.C. 103 have been fully considered and are persuasive. Therefore, the final rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Dent (US 6,091,303).

Claim Objections

2. Claims 1-10 and 16 are objected to because of the following informalities:

Claim 1, line 8: "an" is suggested changing to "said"; and line 9: "a plurality" is suggested changing to "the plurality".

Claim 6 & Claim 7, line 1: "provided" is suggested changing to "wherein the apparatus is composed".

Claim 6 & Claim 16, "UMTS" is suggested spelling out to define the term "UMTS".

Claim 8, line 1: "including" is suggested changing to "further including"; line 3: "including a further" is suggested changing to "comprising a"; line 4: "a further" is suggested changing to "a"; and line 5: "said further" is suggested changing to "said".

Claim 9, line 1: "the further" is suggested changing to "the"; and line 2: "a further" is suggested changing to "a".

Claims 2-5, and 10 are directly or indirectly dependent on the objected claim 1.

Appropriate corrections are required.

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Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1-5, 8-15, and 18-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bjerede et al. (US 5722040) in view of Dent (US 6,091,303).

Regarding **claims 1 & 11**, in FIG.2, Bjerede et al. teaches an apparatus and its methods for producing an RF transmission signal including a plurality of frequency channels (column 1 lines 60-65, column 5 lines 45-50, column 6 lines 22-26, where the apparatus includes multiple frequency channels, the channel spacing is suggested). Referring to Fig. 9,

an IF processor (elemebts 96, 50, 98, 100, 104, 106 and 64) having a first input for receiving a baseband signal from a baseband processor (26) and a second input for receiving a first signal from output of a local oscillator 100 to produce an IF signal to a RF processor 30;

The RF processor (elements 52, 114, 54 and 118) having a first input coupled to the IF processor (28) for receiving the IF signal and a second input for receiving a second signal from the output of a synthesizer 54 to produce an RF transmission signal including a plurality of frequency channels separated by a desired frequency channel spacing (column 5 lines 45-50,

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where the channel spacing is suggested), wherein the local oscillator 100 is the first frequency synthesizer coupled to the second input of the IF processor for providing the first signal;

Bjerede fails to teach that (see the claimed subject matter recited in lines 11-14 of claim1). However, Dent teaches a phase locked loop (12) or frequency synthesizer for baseband signals (I and Q in the modulator 14) to generate an IF signal 42 in Fig. 1 and column 4 lines 20-30, wherein the I and Q signals are mixed with a combining signal 20 generated from the phase-locked loop 12 with a comparison frequency generator 32 to generate the combining signal at one of a plurality of passible frequencies responding to a raster set (Fref/M), then translate the IF signal to a RF signal to the power amplifier 66 for transmission. As the Dent providing the frequency synthesizers to translate the signal to IF and RF to transmit (column 4 lines 35-43), at the time of the invention, it would have been obvious to a person of ordinary skill in the art to implement the frequency synthesizer taught by Dent in the element 100 of Bjerede et al.'s apparatus in order to generate a combining signal at one of plurality of possible frequencies separated from one another by the divider 30 IF for the purpose of reducing the phase noise of the PLL in the radio system (column 1 lines 5-10).

Regarding claims 2-3 & 12-13, the combined/modified Bjerede's apparatus with Dent's teaching teaches the comparison frequency is an integer multiple of the raster component or equal to the raster component (N x Fref/M of Fig. 1 wherein the Fref/M is the raster).

Regarding claims 4-5, 10, 14-15, & 20, the combined/modified Bjerede's apparatus with Dent's teaching teaches an integer/type-1 phase locked loop, however Dent teaches an integer/type-1 phase locked loop (Fig.1 having ÷ N feedback and the phase detector 32 being coupled to the loop filter 36 without use of a charge pump).

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Regarding **claims 8 & 18**, Bjerede et al discloses a second frequency synthesizer coupled to the second input of said RF processor for providing the second combining signal (54 FIG.2) but does not specify the comparison frequency generator in the second frequency synthesizer. However, Dent teaches the phase locked loop for the baseband signal (I and Q in the modulator 14) to generate an IF signal 42 in Fig. 1 and column 4 lines 20-30, wherein the I and Q signal is mixed with the combining signal 20 generated via the phase-locked loop element 12 with the comparison frequency generator element 32 to generate the combining signal at one of a plurality of passible frequencies responding to the raster set (Fref/M), then translate the IF signal to RF signal to the power amplifier 66 to transmit. As the Dent providing the frequency synthesizers to translate the signal to IF and RF to transmit (column 4 lines 35-43), at the time of the invention, it would have been obvious to a person of ordinary skill in the art to implement the frequency synthesizer taught by Dent in the frequency synthesizer 54 of Bjerede et al.'s apparatus to generate RF signal for the purpose of reducing the phase nose of the PLL in the radio system (column 1 lines 5-10).

Regarding claims 9 & 19, in FIG.9 element 54, Bjerede et al discloses the comparison frequency corresponds to a component of the desired frequency channel spacing other than the raster component (column 9 lines 25-33, where the 300k is the desired frequency channel spacing set by RF).

5. Claims 6 & 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bjerede et al. (US 5722040) in view of Dent (US 6,091,303) as applied to claims 1 and 11 above, and further in view of Khlat (US 6069535).

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Regarding claims 6 & 16, Bjerede et al. does not explicitly specify the channel spacing for UMTS, however Khlat teaches the PLL frequency synthesizer for UMTS (FIG.1, column 49-55). At the time of the invention, it would have been obvious to a person of ordinary skill in the art to have the UMTS teaching by Khlat in the Bjerede et al.'s apparatus to provide a UMTS transmitter. The suggestion/motivation for doing so would have been to provide an efficient and low noise fractional-N synthesizers to the UMTS transmitter (column 1 lines 43-45, column 2 lines 13-17). Therefore, it would have been obvious to combine Khlat's teaching with Bjerede et al. to obtain the invention as specified in the claim(s).

6. Claims 7 & 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bjerede et al. (US 5722040) in view of Dent (US 6,091,303) as applied to claims 1 and 11 above, and further in view of Boesch (US 6556545 B1).

Regarding claims 7 & 17, Bjerede et al. does not explicitly specify the channel spacing for WCDMA, however Boesch teaches the phase locked loops/synthesis for WCDMA (60 FIG.2, column 1 lines 15-21, column 4 lines 16-35) provided in a WCDMA transmitter. At the time of the invention, it would have been obvious to a person of ordinary skill in the art to have the WCDMA teaching by Boesch in the Bjerede et al.'s apparatus to provide a WCDMA transmitter. The suggestion/motivation for doing so would have been to provide a WCDMA transmitter with an efficient and low cost radio apparatus (column 2 lines 3-11). Therefore, it would have been obvious to combine Boesch's teaching with Bjerede et al. to obtain the invention as specified in the claim(s).

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Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edith M Chang whose telephone number is 571-272-3041. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jayanti Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Edith Chang November 23, 2004

YOUNG T. TSE PAIMARY EXAMINER